

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Bowles et al.

Art Unit: TBD

Serial No.: TBD

Examiner: TBD

Filing Date: 12/07/00

Docket No.: TI-24521

Title: METHOD FOR UNPATTERNED RESIST ETCH BACK OF SHALLOW
TRENCH ISOLATION REFILL INSULATOR

AT&T
jce825 U.S. PTO
09/732468
12/07/00



INFORMATION DISCLOSURE STATEMENT

December 7, 2000

Assistant Commissioner for Patents
Washington, DC 20231

Dear Sir:

Please make the references listed on the enclosed PTO-1449 of record under 37 C.F.R. 1.56, 1.97, and 1.98 in the patent application identified above. A copy of the listed references is enclosed.

Respectfully submitted,



Jacqueline J. Garner
Attorney for Applicants
Reg. No. 36,144

Texas Instruments Incorporated
P. O. Box 655474, MS 3999
Dallas, Texas 75265
(972)271-1176 or (972) 917-5643
(972)917-4418 Fax